

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(ignition near coil) same microprocesor same bypass same reluctor same distributor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 07:28
L2	1	(ignition near coil) same bypass same reluctor same distributor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 07:28
L3	0	(ignition near coil) same microprocesor same bypass same reluctor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 07:28
L4	0	(ignition near coil) same microprocesor same bypass	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 07:28
L5	0	(ignition near coil) same microprocesor same reluctor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 07:28